

Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

73639

Application ID: 10070091

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Title of Invention: MULTITHREADED PARALLEL

PROCESSOR ARCHITECTURE

First Named Inventor: Gilbert Wolrich

Domestic/Foreign Application: Domestic Application

Filing Date: 2002-02-27

Effective Receipt Date: 2004–12–05

Submission Type: Information Disclosure Statement

Filing Type:

Confirmation number: 7309

Attorney Docket Number: 10559–310US1

Total Fees Authorized:

Digital Certificate Holder: cn=Scott C. Harris,ou=Registered Attorneys,ou=Patent and Trademark

Office,ou=Department of Commerce,o=U.S. Government,c=US

Certificate Message Digest: b365fd2748fe6afb596c1753538580c1f982ff35

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Electronic Version v1.1 Stylesheet Version v1.1.0

DEC 0 5 2004

Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE

Application Number:

Date:

10/070091

2002-02-27

First Named Applicant:

Gilbert Wolrich

Confirmation Number:

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Submitted by:	Elec. Sign.	Sign. Capacity
Scott C. Harris	/SCH/	
Registered Number: 32,030		Attorney

Documents being submitted

us-ids

Files

10559-310US1_1-usidst.xml

us-ids.dtd

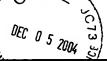
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Comments

IDS # 1 of 4 for U.S. Application Serial No. 10/070,091

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0



Title of Invention

REGISTERSET USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE

Application Number:

10/070091

Confirmation Number:

7309

First Named Applicant:

Gilbert Wolrich Attorney Docket Number: 10559-310US1

Search string:

(3373408 or 3478322 or 3792441 or 3913074 or 3940745 or 4130890 or 4392758 or 4400770 or 4514807 or 4523272 or 4724521 or 4745544 or 4777587 or 4866664 or 5073864 or 5140685 or 5142683 or 5155831 or 5155854 or 5168555 or

5173897 or 5255239 or 5263169 or 5347648 or 5357617 or 5367678 or 5390329 or 5392391 or 5392411 or 5392412 or 5404464 or 5404482 or 5428809 or 5432918 or 5442756 or 5448702 or 5450351 or 5452437 or 5459842 or 5463625 or 5467452 or 5481683 or 5487159 or 5517628 or 5517648 or 5542070 or 5542088 or 5544236 or 5550816 or 5557766 or

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20040205747).pn.



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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Note: Applicant is not required to submit a paper copy of cited US Published Applications

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Signature

Examiner Name	Date

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Title of Invention

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First Named Applicant:

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	Elec. Sign.	Sign. Capacity
Submitted by:	//SCH/	
Scott C. Harris Registered Number: 32,030		Attorney

Documents being submitted

us-ids

Files

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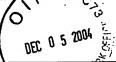
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Comments

IDS # 2 of 4 for U.S. Application Serial No. 10/070,091

ELECTRONICANEORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0



Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE

Application Number:

10/070091

Confirmation Number:

7309

First Named Applicant:

Gilbert Wolrich Attorney Docket Number: 10559-310US1

Search string:

(5568617 or 5574922 or 5574939 or 5592622 or 5606676 or 5613071 or 5613136 or 5623489 or 5627829 or 5630130 or 5644623 or 5649157 or 5659722 or 5680641 or 5689566 or

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5905876 or 5905889 or 5915123 or 5933627 or 5659687).pn.



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US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Signature

 Examiner Name	Date	

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Electronic Filing System (EFS) Data Electronic Patent Application Submission **USPTO** Use Only

73641

Application ID:

10070091

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Title of Invention:

REGISTER SET USED IN

MULTITHREADED PARALLEL

PROCESSOR ARCHITECTURE

First Named Inventor:

Gilbert Wolrich

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Domestic/Foreign Application:

Domestic Application

DEC 0 9 2004

Filing Date: Effective Receipt Date: 2002-02-27 2004-12-05

Technology Center 2100

Submission Type:

Information Disclosure Statement

Filing Type:

Confirmation number:

7309

Attorney Docket Number:

10559-310US1

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Office,ou=Department of Commerce,o=U.S. Government,c=US

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DEC 0 5 2004

Title of Invention

REGIS ER USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE

Application Number:

10/070091

Date:

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First Named Applicant:

Gilbert Wolrich

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Submitted by:	Elec. Sign.	Sign. Capacity
Scott C. Harris	/SCH/	
Registered Number: 32,030		Attorney

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Comments

IDS # 3 of 4 for U.S. Application Serial No. 10/070,091

. ELECTROBLE INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

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Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE

Application Number:

10/070091

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Search string:

(5937187 or 5938736 or 5940612 or 5940866 or 5946487 or 5948081 or 5951679 or 5958031 or 5961628 or 5970013 or 5978838 or 5983274 or 5996068 or 6002881 or 6009515 or 6012151 or 6014729 or 6023742 or 6058168 or 6058465 or 6067585 or 6070231 or 6072781 or 6073215 or 6076158 or 6079008 or 6079014 or 6085215 or 6085294 or 6092127 or 6092158 or 6112016 or 6115811 or 6134665 or 6141689 or 6141765 or 6144669 or 6145054 or 6157955 or 6160562 or

6182177 or 6195676 or 6199133 or 6201807 or 6212542 or 6212611 or 6216220 or 6223207 or 6009505 or 6029228).pn. RECEIVED DEC 0 9 2004

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US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Signature

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Submitted by:	Elec. Sign.	Sign. Capacity	
Scott C. Harris Registered Number: 32,030	/SCH/	Attorney	

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Comments

IDS # 4 of 4 for U.S. Application Serial No. 10/070,091

ELECTRONIC-INFORMATION DISCLOSURE STATEMENT

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	init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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